2. (Amended) The method for improving resolution of a current mode driver as claimed in claim 1, wherein the step of adjusting the full scale current comprises the steps of:

generating an adjustment signal in response to the sensing step; and

applying the adjustment signal to the current mode driver, the adjustment signal causing the current mode driver to adjust the full scale current.

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3. (Amended) The method as claimed in claim 2, wherein the step of applying the adjustment signal to the current mode driver comprises applying at least one predetermined voltage to a corresponding at least one transistor switch.

4. (Amended) The method for improving resolution of a current mode driver as claimed in claim 1, wherein the current control signal comprises a plurality of bits.

5. (Amended) The method as claimed in claim 1, wherein the sensing step comprises determining a condition associated with a phase-locked loop.

6. (Amended) The method as claimed in claim 1, wherein the sensing step comprises determining a condition associated with a delayed locked loop.

7. (Amended) The method as claimed in claim 1, wherein the sensing step comprises the steps of:

applying a PVT independent current to a PVT sensitive load; and

detecting a voltage drop across the PVT sensitive load.

8. (Amended) The method as claimed in claim 1, wherein the sensing step comprises the steps of :

applying a pulse in parallel to a delay line and a first plurality of latches, wherein the delay line comprises a second plurality of delay stages;

coupling an output of a subset of the plurality of delay stages to an input of a corresponding latch from the plurality of latches; and

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decoding an output from the plurality of latches.

9. (Amended) The method as claimed in claim 1, wherein the sensing step comprises sensing a PVT sensitive DC parameter.

10. (Amended) The method as claimed in claim 1, wherein the sensing step comprises sensing a PVT sensitive AC parameter.

11. (Amended) In an output driver that is operable to provide an output that falls within a predetermined range, wherein the output is set in accordance with a current control signal, a method of improving resolution of the output driver, the method comprising the steps of:

applying the current control signal to cause the output driver to sink a full scale current;

providing a PVT detector to sense a characteristic that comprises at least one of a process condition, a voltage condition and a temperature condition;

generating a full scale current adjustment signal at the PVT detector; and

applying the full scale current adjustment signal to alter the full scale current of the output driver.

12. (Amended) The method of improving resolution of an output driver as claimed in claim 11, wherein the step of applying the full scale current adjustment signal comprises coupling the adjustment signal to a digital-to-analog converter.

13. (Amended) The method of improving resolution of an output driver as claimed in claim 12, wherein the adjustment signal is a two-bit signal and the digital-to-analog converter has at least two inputs.

14. (Amended) The method of improving resolution of an output driver as claimed in claim 12, wherein the digital-to-analog converter provides an output signal in response to the adjustment signal.

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